

Features

- Low latency and high throughput USB Host Controller from Renesas
- Low phase noise discrete Discroll-type crystal oscillator
- Ultralow voltage noise LDO regulator for oscillator circuitry and linear LDO for core voltage
- Internal or external direct +5V input for powering external USB peripherals
- Normal and low-profile brackets
- 3 years manufacturer's warranty

Components

- Renesas μPD720202 USB 3.0 chipset
- LT3042 ultralow noise, ultrahigh PSRR RF linear regulator
- High-Q series resonance crystal
- 4 layer, high TG , improved FR4 PCB

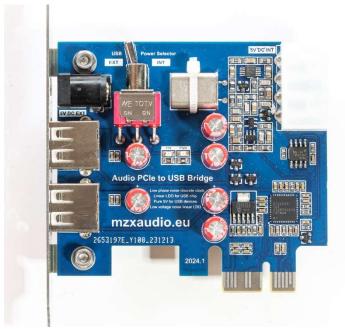
Description

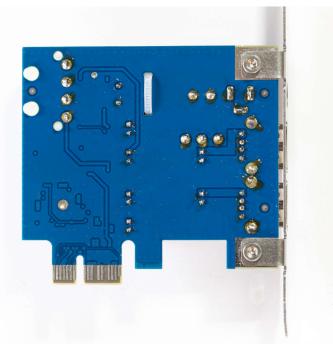
The APCIe2USB+ is intended to be applied in high-end audio PCs for USB audio playback. The low phase noise reference clock, the clean powering of the USB host controller chip, and the external pure powering of the USB device guarantee accurate, smooth dataflow and audiophile sound quality.

Internal or external +5V input for the USB peripheral can be selected by the toggle switch on the board.

Very carefully designed 4 layers improved FR4 printed circuit board (PCB) with surface mounted components ensure minimal signal path and optimal signal integrity.

Product photos (with low profile bracket)







SPECIFICATION		
INPUT	PC bus connector	PCle Gen2 x 1 Lane
	Power Internal	MOLEX, Pin1=+12V (Clock, optional); Pin2-Pin3=GND; Pin4=+5V (USB)
	Power External (USB)	+5VDC for USB, 5.5/2.1 mm DC socket
OUTPUT	Connector	USB 2.0 Type A x 2
OTHERS	PCB Dimension	74 x 69 mm [Width x Height] without bracket
	Weight	TBD grams
ENVIRONMENT	Working temperature	-10 C° to +40 C°
	Working Humidity	20 to 90% RH, non-condensing
PACKAGING	Material	Corrugated paper and foam
	Box content	 APCIe2USB card equipped with normal bracket Additional low-profile bracket
	Dimension	205 x 135 x 45 mm
	Weight	150 grams

Ordering code: APCIe2USB+

Label



CLK Power: 5Vdc / 1.5A CLK Power: 12Vdc / 50mA BUS: PCle Contact BUS: PCle Gen2 x 1 S/N: 3011 HW ver. 2024.1 Built: 2024.15.wk

